



Attorney Docket No. 1450.1019

cofc

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Sho MATSUMOTO

Application No.: 10/098,551

Confirmation No.: 4963

Filed: March 18, 2002

U.S. Patent No.: 6,871,329 *B2*

Issued: March 22, 2005

For: DESIGN SYSTEM OF INTEGRATED CIRCUIT AND ITS DESIGN METHOD AND PROGRAM

ATTENTION

CERTIFICATE OF CORRECTION

BRANCH

Batch No.:

Certificate
SEP 06 2005
of Correction

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

Patentee respectfully request that a Certificate of Correction be issued in the subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistake shown on the attached Form PTO-1050.

Since the mistake is a Patent Office mistake, it is believed that no fee is required.

Respectfully submitted,

STAAS & HALSEY LLP

Date: *Aug. 13, 2005*

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO : 6,871,329 *62*

DATED : March 22, 2005

INVENTOR(S) : Sho MATSUMOTO

Column 12, line 2, (claim 6, line 13) change "base" to --based--.

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PATENT NO. 6,871,329

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